

Driver

Series/Type: V 4.0 Ordering code: Z63000Z2910Z1Z83

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Z63000Z2910Z1Z83

V 4.0

Preliminary data

Intended use

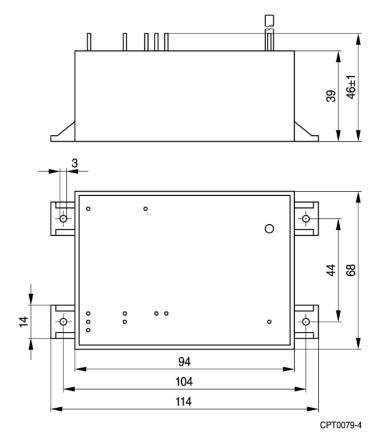
MediPlas Driver

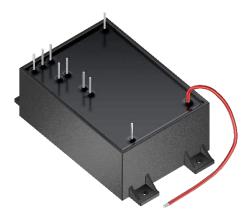
HV driver for DBD loads

Features

- Compact
- Passive cooling
- Broad capacitance range of applicable loads
- Adjustable output voltage
- PWM power control input
- Process monitoring signal
- Overheating with auto recovery

Dimensional drawings in mm







Driver

Preliminary data

Specification

Electrical data

2 6 kV AC (proportional to input voltage)
20 40 kHz (auto resonant)
12.0 27.5 V DC
30 W
HV cable
Pins Ø 1.3 mm
Reverse polarity (auto recovery) Input overvoltage (auto recovery) Input undervoltage (auto recovery) Overtemperature (auto recovery)
High-level input threshold: 2.0 … 50 V Low-level input threshold: -0.3 … 0.8 V (HV output control via PWM up to 100 Hz or DC ON/OFF)
PlasmaOk (attenuated analog feedback signal)
< 5 ms (load dependent)
6 A @ 30 V (< 500 μs); 40 A peak @ 30 V
2 90 pF
0.3 1 ΜΩ
< 2.4 mJ

Dimensions and weight

Length	114 mm
Width	68 mm
Height	39 mm (without HV cable)
Cable length	160 ±5 mm
Mounting hole diameter	3 mm
Mounting hole spacing, length	104 mm
Mounting hole spacing, width	44 mm
Weight	~ 460 g

V 4.0



MediPlas Driver Z63000Z2910Z1Z83

V 4.0

Preliminary data

Environmental

Operating temperature range	5 40 °C
Humidity	< 80%, non-condensing
Maximum operating altitude	3000 m



V 4.0

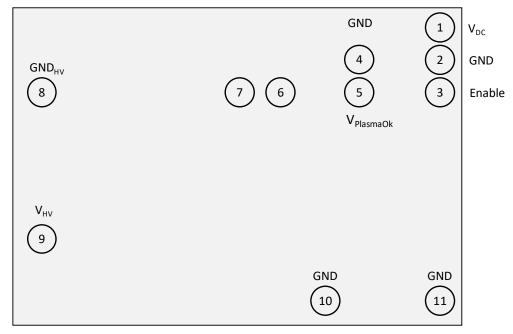
Z63000Z2910Z1Z83

MediPlas

Driver

Preliminary data

Pin configuration



Pin no.	Name	Comments
1	V _{DC}	Supply voltage
2	GND	Ground potential (DC)
3	Enable	PWM or DC ON/OFF
4	GND	Ground potential (DC)
5	PlasmaOk	PlasmaOk signal voltage
6	Pin 1 for additional primary capacitance	Capacitance adjustment ¹⁾
7	Pin 2 for additional primary capacitance	Capacitance adjustment ¹⁾
8	GND _{HV}	Ground potential for high voltage
9	V _{HV}	High voltage
10	GND	Ground potential (DC)
11	GND	Ground potential (DC)

¹⁾ Primary capacitance to be matched to load capacitance for resonance frequency alignment

Table 1: Pin input and output description



Driver

Preliminary data

Control/diagnostic signals

VDC

The "V_{DC}" input is the supply pin of the driver. Besides supplying the internal driver logic, the voltage level at "V_{DC}" also determines the amplitude of the high voltage output. The AC voltage amplitude "V_{HV}" is directly proportional to the "V_{DC}" voltage level (see Figure 2: V_{HV} output voltage dependency of the input voltage).

Enable

The CMOS compatible "Enable" input directly controls the high voltage output V_{HV} . V_{HV} can be activated by forcing the "Enable" pin to high state (> 2.0 V). Forcing the "Enable" to low state (< 0.80 V) will switch off " V_{HV} ". A PWM can be used to control the output power of the driver to stabilize the operating point (self-heating of the driver and/or the load). If remote control or the PWM feature are not needed, connect "Enable" to " V_{DC} " for continuous high voltage output operation. For electrical properties see Table 2: Electrical properties of "Enable" signal.

Parameters	;	Min.	Тур.	Max.	Unit
ENTHH	High-level input threshold	2.0	-	50	V
ENTHL	Low-level input threshold	-0.3	-	0.80	V
EN _{PWM}	PWM frequency	-	50	100	Hz
END	Duty cycle	$\frac{1.06 \ ms \ \times \ EN_{PWM}}{0.01}$	-	100	%
EN _{DEL}	Enable delay time	0.58	0.75	1.06	ms

Table 2: Electrical properties of the "Enable" signal

PlasmaOk

The "PlasmaOk" signal is proportional to the gap discharge intensity. It is only provided during active gap discharge cycles and is load and process dependent. When the "Enable" signal is in the HIGH state, the "PlasmaOk" signal shows an analog signal proportional to the " V_{HV} " gap discharge current. It is recommended to terminate the "PlasmaOk" signal with at least 100 k Ω . A smaller termination resistance will result in an attenuation of the signal level. If the "PlasmaOk" signal is not required, it can be left floating (e.g. non-discharging loads or if a feedback signal is not necessary).

V 4.0



Driver

Z63000Z2910Z1Z83

V 4.0

Preliminary data

Driver states

Driver State	Inputs		Outputs			
	VDC	Enable	V _{HV}	PlasmaOk		
DISABLED	V_DC< 5.1V	х	low	low		
	5.1V <v_dc< v_dc<sub="">UVLOT</v_dc<>	х	low	low		
	V_DC> V_ DC _{OVPT}	х	low	low		
	V_DCUVLOT <v_dc< td="" v_dcovpt<=""><td>low</td><td>low</td><td>low</td></v_dc<>	low	low	low		
ENABLED	V_DCUVLOT <v_dc< td="" v_dcovpt<=""><td>high</td><td>low</td><td colspan="2">low</td></v_dc<>	high	low	low		
	V_DC _{UVLOT} <v_dc< v_dc<sub="">OVPT</v_dc<>	high	HV-out	low ²⁾		
	V_DCUVLOT <v_dc< td="" v_dcovpt<=""><td>high</td><td>HV-out</td><td>Analog out ³⁾</td></v_dc<>	high	HV-out	Analog out ³⁾		

²⁾ Expected behavior for non-discharging loads

³⁾ Expected behavior for gap discharging loads

Table 3: Driver states and their assignment to inputs and outputs

Protection functions

Parameters		Min.	Тур.	Max.	Unit	Reset
V_DC _{RP}	Reverse polarity input voltage	-60	-	+60	V	Auto recovery
V_DC _{OVPT}	Overvoltage protection threshold	27.8	30.0	32.5	V	Auto recovery
V_DC _{OVPH}	Overvoltage protection hysteresis	-	-3.7	-	V	
V_DC _{UVLOT}	Undervoltage lockout threshold	8.4	9.4	10.4	V	Auto recovery
V_DC _{UVLOH}	Undervoltage lockout hysteresis	-	-1.4	-	V	Auto recovery
T _{OVT}	Overtemperature protection	-	85	-	°C	Auto recovery
Тоутн	Overtemperature protection hysteresis	-	10	-	°C	Auto recovery

Table 4: Electrical properties of protection functions



V 4.0

Z63000Z2910Z1Z83

MediPlas

Driver

Preliminary data

Permissible load parameters

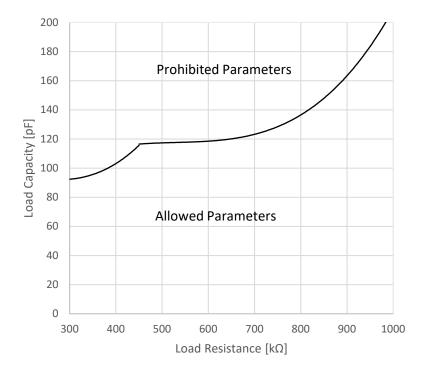


Figure 1: Range of permissible load parameters



Driver

Z63000Z2910Z1Z83 V 4.0

Preliminary data

Input/output voltage dependency

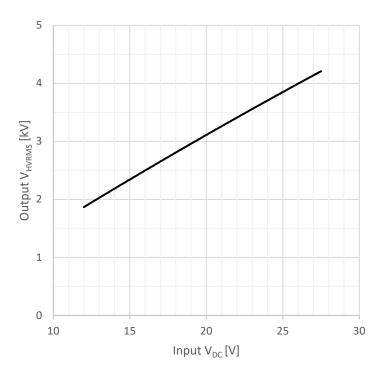
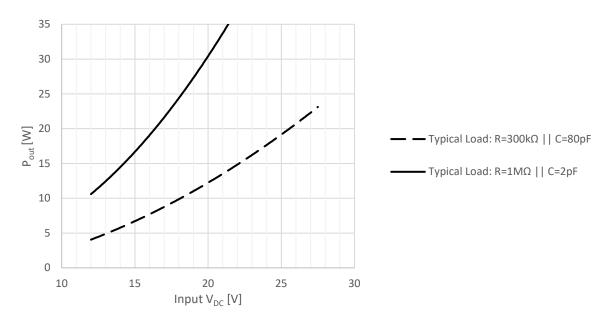


Figure 2: V_{HV} output voltage dependency of the input voltage



Input voltage dependency on output apparent power

Figure 3: Output apparent power in dependence of the input voltage for typical loads



V 4.0

Z63000Z2910Z1Z83

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Preliminary data

Troubleshooting

- 1) Turn OFF supply voltage on "V_{DC}".
- 2) Check if "V_{DC}" and "Enable" are correctly connected to the driver.
- 3) Check if " V_{HV} " and "GND_{HV}" are correctly connected to the load.
- 4) Check if one of the protection functions is active (see Table 4: Electrical properties of protection functions)
- 5) Check whether the load has a short circuit or the maximum permissible load parameters are exceeded (see Figure 1: Range of permissible load parameters).
- 6) Check "PlasmaOk" signal termination and connection to external signal measuring device/processing unit (see Control/diagnostic signals).
- 7) Turn ON supply voltage on "V_{DC}". Do not exceed maximum supply voltage of 27.5 V.
- 8) Increase supply voltage until an analog output voltage is measurable on "PlasmaOk" during "Enable" high phases. A "PlasmaOk" signal is only detectable at active gap discharges. Do not exceed the maximum DC power consumption of 30 W!



Driver

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Z63000Z2910Z1Z83

V 4.0

Preliminary data

Cautions and warnings

i	Important! The products delivered are engineering samples, which are not intended for commercial use in series products of the purchaser. TDK assumes no warranty. Any use is at the sole risk of the purchaser. In case of any questions, please contact TDK.
	Only TECHNICALLY QUALIFIED SERVICE PERSONNEL familiar with the principles of electrical safety should operate this supply. The power supply SHOULD NOT BE EXPOSED TO WATER OR MOISTURE OR DUSTY ENVIRONMENTS (pollution degree 2). Electrical safety must be always maintained.
	Danger due to high voltage! Always ensure that the high voltage parts are connected correctly. Do not open the device. If the device is damaged, disconnect the voltage supply and contact the manufacturer.
	Do not touch the device and connecting cables during operation. All parts of the device can carry high voltages during operation. After disconnecting the supply voltage, wait at least one minute and make sure by measurement that the input capacitors are discharged before touching the device.
	Do not use the device in a flammable or explosive atmosphere.
	Do not operate the device if it is damaged.
	The device is designed for indoor use only. Splashing water and/or excessive humidity may cause the device to destruct or fail.
	Never attempt to operate the power supply in any manner not described in this manual. Never remove DANGER and WARNING labels from the device. Replace lost or damaged labels immediately.
	When operating this device with plasma reactors (intended use), high EMI with other devices can/will occur. When used with a plasma reactor, it is recommended to keep the connecting cables short and shield both devices accordingly.



Driver

Z63000Z2910Z1Z83

V 4.0

Preliminary data

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